

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	49	("5354695").URPN.	USPAT	OR	ON	2007/01/19 17:06
L6	9	"672961"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/01/19 17:32
L7	3	6 and control adj logic	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/01/19 17:26
L8	6	mcm with stack\$3	EPO; JPO	OR	ON	2007/01/19 17:37
L9	2	("5016138").PN.	US-PGPUB; USPAT; USOCR; DERWENT	OR	OFF	2007/01/19 17:30
L10	12778	layer with stack\$3	EPO; JPO	OR	ON	2007/01/19 17:37
L11	3	10 and 3-d	EPO; JPO	OR	ON	2007/01/19 17:38
L12	0	10 and control adj logic	EPO; JPO	OR	ON	2007/01/19 17:38
L13	10	10 and logic adj circuit\$3	EPO; JPO	OR	ON	2007/01/19 17:38

*** It is now 1/19/2007 4:37:25 PM ***

[File 2] INSPEC 1898-2007/Dec W4

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Set	Items	Description
S1	5567	S (SUBSTRATE? ? OR LAYER??? OR CHIP? ?) (3N) STACK???
S2	5154	S (SUBSTRATE? ? OR LAYER???) (3N) STACK???
S3	3678	S LAYER? ? (2N) STACK???
S4	101	S S3 AND PACKAG????
S5	17	S S4 AND PY=1980:1997
S6	4231	S (SUBSTRATE? ? OR LAYER??? OR CHIP? ? OR FILM? ? OR MCM? ?) (1N) STACK???
S7	138	S MODULE (2N) STACK???
S8	4335	S S6:S7
S9	31	S S8 AND VERTICAL () INTERCONNECT?????
S10	2	S S8 AND VERTICAL () INTER
S11	33	S S9:S10
S12	9	S S11 AND PY=1980:1997
S13	3272	S (MULTILAYER??? OR MULTI () LAYER???) AND PACKAG????
S14	1435	S S13 AND PY=1985:1997
S15	440	S S14 AND INTERCONNECT?????
S16	310	S S15 AND (MEMOR??? OR IC OR INTEGRATED () CIRCUIT???)

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